NDTL03N150C

N-Channel Power MOSFET 1500V, 2.5A, 10.5Ω, TO-3P-3L

Features

- On-resistance $R_{DS}(on)=8\Omega(typ.)$
- Input Capacitance Ciss=650pF(typ.)
- 10V drive

Specifications

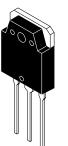
Absolute Maximum Ratings at Ta = 25°C



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TO-3P-3L



Parameter	Symbol	Conditions	Ratings	Unit
Drain to Source Voltage	VDSS		1500	V
Gate to Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		2.5	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	5	А
Allowable Power Dissipation	PD		2.5	W
		Tc=25°C	140	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		- 55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		34	mJ
Avalanche Current *2	IAV		2.5	А

*1 VDD=50V, L=10mH, IAV=2.5A (Fig.1)

*² L≤10mH, Single Pulse

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

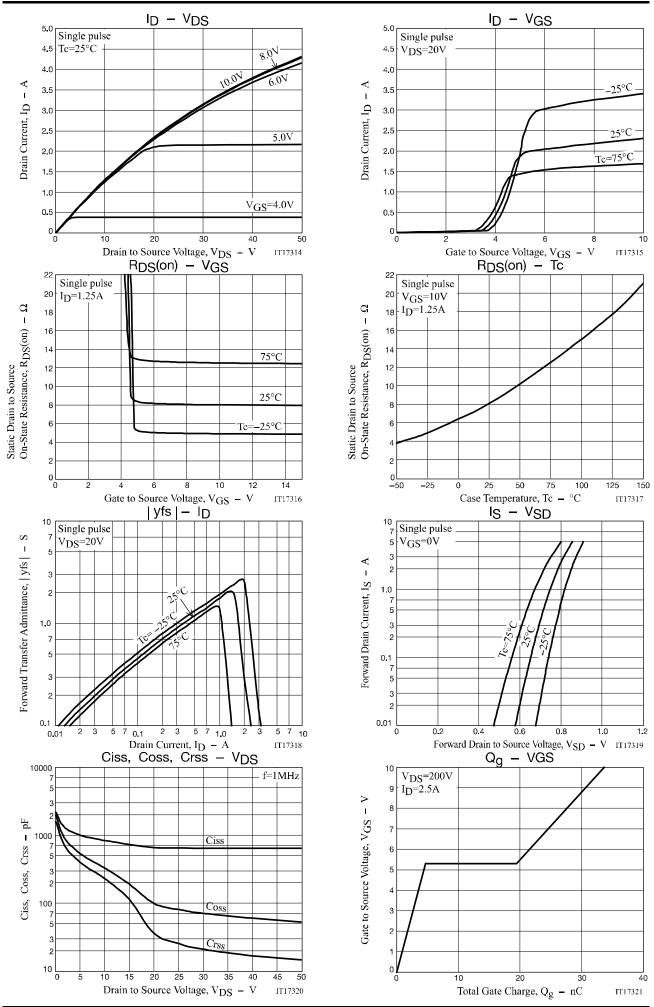
Electrical Characteristics at Ta = 25°C

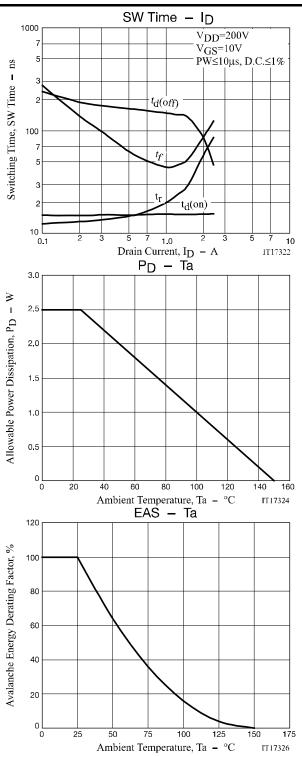
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I _D =10mA, V _{GS} =0V	1500			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =1200V, V _{GS} =0V			1	mA
Gate to Source Leakage Current	IGSS	V _{GS} =30V, V _{DS} =0V			±100	nA
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	2		4	V
Forward Transfer Admittance	yfs	V _{DS} =20V, I _D =1.25A		1.9		S
Static Drain to Source On-State Resistance	R _{DS} (on)	I _D =1.25A, V _{GS} =10V		8	10.5	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		650		pF
Output Capacitance	Coss			70		pF
Reverse Transfer Capacitance	Crss			20		pF
Turn-ON Delay Time	t _d (on)	See Fig.2		15		ns
Rise Time	tr			24		ns
Turn-OFF Delay Time	t _d (off)			140		ns
Fall Time	tf			47		ns
Total Gate Charge	Qg			34		nC
Gate to Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =2.5A		4.7		nC
Gate to Drain "Miller" Charge	Qgd	1		15		nC
Diode Forward Voltage	VSD	I _S =2.5A, V _{GS} =0V		0.8	1.5	V
Reverse Recovery Time	trr	See Fig.3		350		ns
Reverse Recovery Charge	Qrr	IS=2.5A, VGS=0V, di/dt=100A/µs		2220		nC

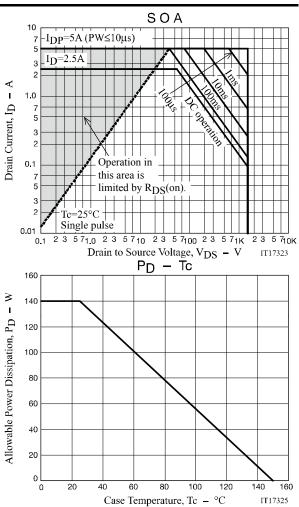
ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

NDTL03N150C







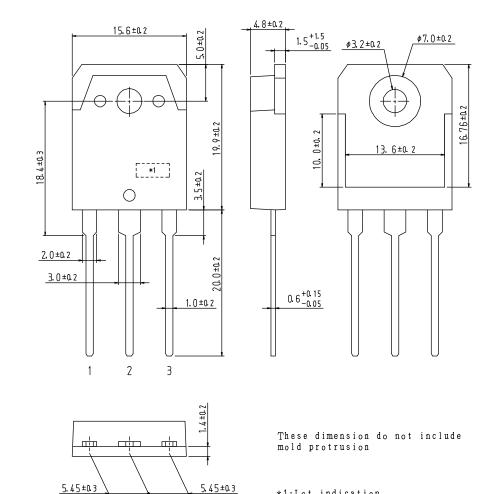
Package Dimensions

NDTL03N150CG

TO-3P-3L

CASE 340AF **ISSUE O** Unit : mm

- 1: Gate
- 2: Drain
- 3: Source



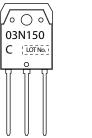
*1:Lot indication

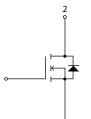
Ordering & Package Information

Device	Package	Shipping	note
NDTL03N150CG	TO-3P-3L, SC-65, SOT-199, TO-247	30 pcs. / tube	Pb-Free

Marking

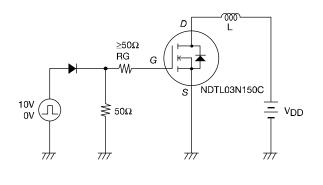


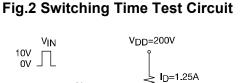




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Fig.1 Unclamped Inductive Switching Test Circuit





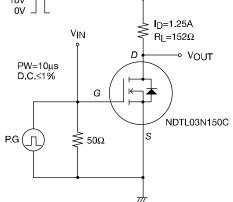
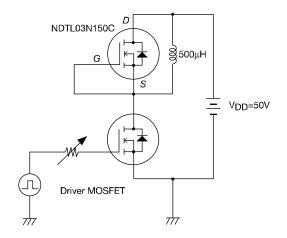


Fig.3 Reverse Recovery Time Test Circuit



Note on usage : Since the NDTL03N150C is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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